

2111

PATENT
Attorney Docket No.: SONY-15100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jon Ebbe Brelín

Serial No.: 09/972,488

Filed: October 4, 2001

For: **A METHOD OF AND APPARATUS
FOR UTILIZING EXTENDED
AV/C COMMAND FRAMES
INCLUDING STATUS INQUIRY,
NOTIFY INQUIRY AND
CONTROL INQUIRY COMMAND
TYPES**

Group Art Unit: 2111

Examiner: Ray, Gopal C.

TRANSMITTAL LETTER

162 N. Wolfe Road
Sunnyvale, CA 94086
(408) 530-9700

Customer No. 28960

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Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed please find a Supplemental Information Disclosure Statement, for the Information Disclosure Statement, and Form PTO-1449, including copies of the references contained thereon, for filing in the U.S. Patent and Trademark Office. Also, attached is a check in the amount of \$180.00 to cover the Supplemental Information Disclosure Statement.

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. **An originally executed duplicate of this transmittal is enclosed for this purpose.**

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: September 28, 2004

By: Jonathan O. Owens
Jonathan O. Owens
Reg. No.: 37,902

Attorneys for Applicant

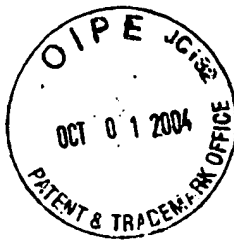
CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450

HAVERSTOCK & OWENS LLP.

Date: 9-28-04

By: Nicole H. Haverstock



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SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT

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Sunnyvale, CA 94086
(408) 530-9700

OCT 12 2004

Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicant has become aware of the following printed publications which may be material to the examination of this application:

- U.S. Patent No. 4,780,821;
- U.S. Patent No. 4,922,486;
- U.S. Patent No. 5,020,054;
- U.S. Patent No. 5,077,732;
- U.S. Patent No. 5,117,070;
- U.S. Patent No. 5,367,679;
- U.S. Patent No. 5,394,522;
- U.S. Patent No. 5,422,883;

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01 FC:1806 180.00 OP

Adjustment date: 10/05/2004 SHINASS1
10/04/2004 SHINASS1 00000019 09972448
01 EG:1806 -180.00-OP

10/05/2004 SHINASS1 00000088 09972488
01 FC:1806 180.00 OP

- U.S. Patent No. 5,436,898;
- U.S. Patent No. 5,446,733;
- U.S. Patent No. 5,471,474;
- U.S. Patent No. 5,479,385;
- U.S. Patent No. 5,495,481;
- U.S. Patent No. 5,499,018;
- U.S. Patent No. 5,500,934;
- U.S. Patent No. 5,524,213;
- U.S. Patent No. 5,528,513;
- U.S. Patent No. 5,548,722;
- U.S. Patent No. 5,557,724;
- U.S. Patent No. 5,574,867;
- U.S. Patent No. 5,579,496;
- U.S. Patent No. 5,632,016;
- U.S. Patent No. 5,650,775;
- U.S. Patent No. 5,659,373;
- U.S. Patent No. 5,682,489;
- U.S. Patent No. 5,694,555;
- U.S. Patent No. 5,719,942;
- U.S. Patent No. 5,781,703;
- U.S. Patent No. 5,815,678;
- U.S. Patent No. 5,933,430;
- U.S. Patent No. 5,991,520;
- U.S. Patent No. 6,055,641;
- U.S. Patent No. 6,094,681;
- U.S. Patent No. 6,233,611 B1;
- U.S. Patent No. 6,359,557 B2
- U.S. Patent No. 6,401,119 B1
- U.S. Patent No. 6,564,295 B2
- U.S. Patent No. 6,584,502 B1
- European Patent No. 0 577 054 A1;
- European Patent No. 0 631 247 A2;
- European Patent No. 0 789 502 A2;

- "The IEEE 1394 High Speed Serial Bus" by R.H.J. Bloks, 1996, pages 209-216;
- "A Bus on a Diet-The Serial Bus Alternative, An Introduction to the P1394 High Performance Serial Bus", Michael Teener, Apple Computer, Inc., Santa Clara, CA., Published February 24, 1992, pages 316-321;
- "P1394 Standard for a High Performance Serial Bus", Copyright 1995, by The Institute of Electrical and Electronic Engineers, Inc. pages 1-384; and
- "AV/C Digital Interface Command Set General Specification", by 1394 Trade Association, Version 3.0, April 15, 1998, pages 1-91.

This Supplemental Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,
HAVERSTOCK & OWENS LLP

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HAVERSTOCK & OWENS LLP

Date: 9-28-04 By: [Signature]

FORM PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark Office

Attorney Docket No.: SONY-15100

Serial No.: 09/972,488

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Use Several Sheets If Necessary)

Applicants: Jon Ebbe Brelin

(37 CFR § 1.98(b))

Filing Date: October 4, 2001

Group Art Unit: 2111

PATENT DOCUMENTS

Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date
	AA	4,780,821	10/25/88	Crossley	364	200	07/29/86
	AB	4,922,486	05/01/90	Lidinsky et al.	370	60	03/31/88
	AC	5,020,054	05/28/91	May, Jr.	370	94	06/23/89
	AD	5,077,732	12/31/91	Fischer et al.	370	85.4	07/24/90
	AE	5,117,070	05/26/92	Ueno et al.	178	2 R	10/09/90
	AF	5,367,679	11/22/94	Khaira	395	650	12/30/92
	AG	5,394,522	02/28/95	Sanchez-Frank et al.	395	159	09/13/93
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	AL	5,479,385	12/26/95	Hoekstra	369	24	08/04/93
	AM	5,495,481	02/27/96	Duckwall	370	85.2	09/30/94
	AN	5,499,018	03/12/96	Welmer	340	825.07	05/22/95
	AO	5,500,934	03/19/96	Austin et al.	395	755	10/04/94
	AP	5,524,213	06/04/96	Dais et al.	395	200.170	01/25/95
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	AU	5,579,496	11/26/96	Van Steenbrugge	395	567	06/07/95
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	BA	5,719,942	02/17/98	Aldred et al.	380	49	03/21/95
	BB	5,781,703	07/14/98	Desai et al.	395	54	09/06/96
	BC	5,815,678	09/29/98	Hoffman et al.	395	309	07/14/95
	BD	5,933,430	08/03/99	Osakabe et al.	370	395	08/09/96
	BE	5,991,520	11/23/99	Smyers et al.	395	280	02/02/96
	BF	6,055,641	04/25/00	Konaka et al.	713	320	10/27/97
	BG	6,094,681	07/25/00	Shaffer et al.	709	224	03/31/98
	BH	6,233,611 B1	05/15/01	Ludtke et al.	709	223	05/08/98
	BI	6,359,557 B2	03/19/02	Bilder	340	531	01/26/98
	BJ	6,401,119 B1	06/04/02	Fuss et al.	709	224	09/18/98

Examiner:

Date Considered:

EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark Office

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U.S. PATENT DOCUMENTS

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	BK	6,564,295 B2	05/13/03	Okabayashi et al.	711	114	09/20/01
	BL	6,584,502	06/24/03	Natarajan et al.	709	224	06/29/99

FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation	
							Yes	No
	BM	EP 0 577 054 A1	01/05/94	EP	H04N	7/18	X	
	BN	EP 0 631 247 A2	06/03/94	EP	G06F	15/44	X	
	BO	EP 0 789 502 A2	09/13/97	EP	H04Q	11/04	X	

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

	BP	"The IEEE-1394 High Speed Serial Bus" by R.H.J. Bloks, 1996, pages 209-216						
	BQ	"A Bus on a Diet-The Serial Bus Alternative ,An Introduction to the P1394 High Performance Serial Bus", Michael Teener, Apple Computer, Inc., Santa Clara, CA ., Published February 24, 1992, pages 316-321.						
	BR	"P1394 Standard for a High Performance Serial Bus", Copyright 1995, by The Institute of Electrical and Electronic Engineers, Inc., pages 1-384.						
	BS	"AV/C Digital Interface Command Set General Specification", by 1394 Trade Association ,Version 3.0, April 15, 1998, pages 1-91.						
	BT							
	BU							
	BV							
	BW							
	BX							
	BY							

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